Amendments to the Specification

Please replace paragraphs [0017-18] in the specification with the following amended paragraphs:

[0017]   Figure 2 illustrates a computer system that may be used in conjunction with at least one embodiment of the invention. A processor 205 accesses data from a cache memory 210 and main memory 215. Illustrated within the processor of Figure 2 is the location of one embodiment of the invention 206. However, embodiments of the invention may be implemented within other devices within the system, as a separate bus agent, or distributed throughout the system. The main memory may be dynamic random-access memory (DRAM), a hard disk drive (HDD) 220, or a ~~memory source 230~~ memory source located remotely from the computer system containing various storage devices and technologies. The cache memory may be located either within the processor or in close proximity to the processor, such as on the processor's local bus 207. Furthermore, the cache memory may be composed of relatively fast memory cells, such as six-transistor (6T) cells, or other memory cells of approximately equal or faster access speed.

[0018]   Figure 3 illustrates a bus agent in which at least one embodiment of the invention may be used. Particularly, Figure 3 illustrates a ~~microprocessor 301~~ microprocessor 300 that contains one or more portions of at least one embodiment of the invention 305. Further illustrated within the microprocessor of Figure 3 is an ~~execution unit 310~~ execution unit 320 to perform operations, such as store operations, within the microprocessor. The exact or relative location of the execution unit and

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portions of embodiments of the invention are not intended to be limited to those illustrated within Figure 3.

Please replace paragraph [0029] in the specification with the following amended paragraph:

[0029]   Any or all portions of the embodiments of the invention illustrated herein may be implemented in a number of ways, including, but not limited to, logic using complimentary metal-oxide-semiconductor (CMOS) circuit devices (hardware), instructions stored within a storage medium (software), which when executed by a machine, such as a microprocessor, cause the microprocessor to perform operations described herein, or a combination of hardware and software. ~~References to "microprocessor" or "processor" made herein are intended to refer to any machine or device that is capable of performing operations as a result of receiving one or more input signals or instructions, including CMOS devices.~~

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